



CORRIGENDUM - VI

Tender No. TENDER/2023-24/110

Date of Publication: 03-10-2023

Indian Institute of Technology Palakkad Invites Tender under Two-bid system for the:

**SUPPLY, INSTALLATION, TESTING AND COMMISSIONING OF
HIGH PERFORMANCE COMPUTING FACILITY**

Sr. No.	Reference	Tender Description	Corrigendum
1.	Last Date/Time for submission of ONLINE Bids	25-10-2023, 15:00 hours	01-01-2024, 15:00 hours
2.	Opening of Technical Bids	25-10-2023, 15:15 hours	01-01-2024, 15:15 hours
3.	Annexure-I Technical Specifications for Proposed HPC Cluster Page No. 9 Sr. No. 1.2	CPU only Compute Nodes: QTY 48	CPU only Compute Nodes: QTY 48 Note: IIT Palakkad reserves the right to increase or reduce the quantity of the compute nodes mentioned in the "SI.no 1.2 in the BOQ" at the time of order placement of the successful bidder.

Note: The request for a separate quote for an extended warranty has been excluded from the BoQ Commercial Bid document. The revised BoQ Commercial Bid document has been uploaded.

All other Terms and Conditions of the Tender remain unchanged.

**REGISTRAR
IIT PALAKKAD**